1762

DOCKET NO.: CFMT-0255

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

An Re Application of:

Steven Verhaverbeke

Serial No.: 10/074,516

Group Art Unit: 1762

Filing Date: February 13, 2002

Examiner: Not Yet Assigned

For: METHOD FOR ETCHING ELECTRONIC COMPONENTS CONTAINING

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DATE OF DEPOSIT: 5/03/02

I HEREBY CERTIFY THAT THIS PAPER IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL, POSTAGE PREPAID ON THE DATE INDICATED ABOVE AND IS ADDRESSED TO THE ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTONDC 20231.

WIP PR

TYPED NAME: **David R. Bailey** REGISTRATION NO.:**35,057**

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §1.56 and in accordance with 37 C.F.R. §§1.97-1.98, information relating to the above-identified application is hereby disclosed. Inclusion of information in this statement is not to be construed as an admission that this information is material as that term is defined in 37 C.F.R. §1.56(b).

In accordance with §1.97(b), since this Information Disclosure Statement is being

filed either within three months of the filing date of the above-identified application, within three months of the date of entry into the national stage of the above identified application as set forth in §1.491, before the mailing date of a first Office Action on the merits of the above-identified application, or before the mailing date of a first office action after the filing of request for continued examination under §1.114, no additional fee is required.

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			(a), this Informa	tion Disclosure State	ment is being
	filed in co	nnection with	\square the first or [□second After Final	Submission,
	therefore:				
	□ Cer	tification in Acc	ordance with §1	.97(e) is attached; or	
	☐ The	fee of \$180.00	as set forth in §1	.17(p) is attached.	
	In accordan	ace with §1.97(c)), this Information	n Disclosure Statemer	nt is being filed
	after the pe	riod set forth in	§1.97(b) above	but before the mailing	g date of either
				Allowance under §1.31	
				the application, there	
				with §1.97(e) is attac	
		The fee of §	§180.00 as set fo	rth in §1.17(p) is attac	ched.
	In accorda	nce with §1.97(d	l), this Informatio	on Disclosure Stateme	nt is being filed
	after the r	nailing date of	either a Final A	Action under §1.113	or a Notice of
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				ion in Accordance wit	
			0.00 as set forth		
\boxtimes	Copies of	each of the re	ferences listed o	n the attached Form	PTO-1449 are
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	Copies of	references listed	on the attached F	Form PTO-1449 are en	closed herewith
	EXCEPT	THAT:			
	□ In	view of the volu	ıminous nature o	f references [list as ap	propriate], and
	th	e likelihood that	these references	are available to the E	Examiner, copies
	ar	e not enclosed h	nerewith.		

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	In accordar	ace with §1.98(d), copies o	f the following references listed on
	the attached	l Form PTO-1449 are not e	nclosed herewith because they were

previously cited by or submitted to the U.S. Patent and Trademark Office in patent application(s) for which a claim for priority under 35

U.S.C.§120 have been made in the instant application:

Copies of references [list as appropriate] listed on the attached Form PTO-1449 were previously cited by or submitted to the Patent and , filed Trademark Office in prior application Serial No.

If any of the foregoing publications are not available to the Examiner, Applicant will endeavor to supply copies at the Examiner's request.

Please charge any deficiency or credit any overpayment to Deposit Account No. 23-3050. This form is submitted in duplicate.

There are no listed references which are not in the English language.

Date: 5/3/02

David R. Bailey

Registration No. 35,057

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	Form :	PTO-1449 Modifi	Docket No. CFMT-0255	Serial No. 10/074,516		
IAY 0 9 2002 (į (Patent and Publication Cited by Applicant weral sheets if necessary	Applicant Steven Verhaverbeke			
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4	BG	09/930,009	08/15/01	Bay, et al.		
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i	AB	Horiki, H. et al., "Wet Etch Cleaning Water, Volume 1, Chapter 3, 805-819		ogy Handbook, Ultra Pure
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-	AD	Werner Kern, "Overview and Evolute Cleaning Technology", Chapter 1,		Wafer Contamination and
<u>۔</u>	AE	Verhaverbeke, S. et al., "Scientific ris Semiconductor Pure Water and Chen Clara, Ca., March 4-7, 1996, 14 page	nicals Conference, Bal	acro and microscale", azs, M.J. (Ed.), Santa
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